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PATENT NUMBER

**U.S. UTILITY Patent Application**

O.I.P.E. <i>M.K.</i>	PATENT DATE SCANNED: 7/9/02 Q.A. <i>SS</i>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/899436	D	710	104	2003 2111	<i>WONG A</i>
APPLICANTS Harry Chue Garry Tsang					
TITLE System and method for efficiently implementing an electronic device architecture					

**ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner) _____ (Date) _____				
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE FEE	
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